# **S51KQ** software for microcontrollers & PC

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### UNIPLL project (CQ-ZRS 3/96):

File name	band RTX ME L	display MHz	VCO frq MHz	<u>Q2</u>	<u>JPI</u>	step	PLL_ic_	rem / IF	<u>Softvare</u>
UNII0GHz	3cm TX 32 -	10000-10484.8	2500.0-2621.2	10.24	AB	I.6MHz	SDA-6102	ATV	v1.30
UNI5GHz	6cm TX 32 -	5598-6000	1866.0-2000.0	10.24	AB		SDA-6102	ATV	v1.30
UNII3T	13cm TX 32 -	2300-2450	2300.0-2450.0	10.24	AB		SDA-6102	ATV	v1.00
<b>UNII3PTL</b>	13cm TX 64 -	1000-2700	1000.0-2700.0			500kHz	TSA-5055T	ATV	v1.30
UNI23T	23cm TX 32 -	1200-1350	1200.0-1350.0	8.00	AB	500kHz	TSA-5511	ATV	v1.00
UNI23TL	23cmTX 32 *	1200-1350	1200.0-1350.0	8.00	AB	500kHz	TSA-5511	ATV	v1.30
UNI70TL	70cmTX 32 *	430- 440	430.0- 440.0	6.40	BC	25kHz	TSA-5511		
UNI70RL	70cm RX 32 *	400- 465	389.3- 454.3	6.40	BC	25kHz	TSA-5511	IF -10.7	
UNI70HL	70cm RX 32 *	400- 465	355.0- 420.0	6.40	BC	25kHz	TSA-5511	IF -45.0	
UNI70WL	70cm RTX 32 *	430- 440	394.0- 404.0	6.40	AB	50kHz	TSA-5511	WBFM36	
UNI-TVL	Im TX 32 *	300- 400	300.0- 400.0	6.40	BC	25kHz	TSA-5511	TV	v1.30
UNI2MTL	2m TX 32 *	130- 160	130.0- 160.0	6.40	BC	25kHz	TSA-5511		
UNI2MRL	2m RX 32 *	130- 165	119.3- 154.3	6.40	BC	25kHz	TSA-5511	IF -10.7	
UNIAVIO	avio RX 64 $^{st}$	108- 140	97.3- 129.3	6.40	BC	25kHz	TSA-5511	IF -10.7	
UNI3MTL	3m TX 32 *	88- 108	88.0- 108.0	6.40	AB	50kHz	TSA-5511		
<b>UNI3MT</b>	3m TX 32 -	88- 108	88.0- 108.0	6.40	AB	50kHz	TSA-5511		
<b>UNI3MRL</b>	3m RX 64 *	87- 108	76.3- 97.3	6.40	AB	50kHz	TSA-5511	IF -10.7	
<b>UNI3MRLP</b>	3m RX 64 *	87- 108	97.7- 118.7	6.40	AB	50kHz	TSA-5511	IF +10.7	

Important: TSA-5511 is build on UNIPLL pcb, insted of SDA-6102 &TSA-5055T, both must be mounted nearVCO!

# Other projects based on PIC 16F84:

File name	project	<u>CQ-ZRS</u>	<u>mode</u>	<u>usage</u>	<u>remarks</u>	<u>Software</u>
LANCI PCGENX PCLOCK2 PCVOICE PCCAM	LANC PCGENX PCGENX PCGENX PCGENX	- 2/1996 - - -	D8 & DV remote DTMF/KEYB DTMF Voice BEACON CAMERA contrl	Cam. ATV FM rpt ATV	Tiny Camcorder remote controller cgen 2,3,4,5,6 controler, I2C bus remote switch 3 voice msg.,AUTO + DTMF remote DTMF remote, 2x ON/OFF + L,R	vI.10 vI.01 vI.00
ATVRX2	ATVRX2	6/1997	PLL TSA5055T	ATV	850-2050 Mhz FM PLL receiver	v1.30
VIDEO84	VIDEO84	2/1997	B/WVgen ATV	test video	o gen.	
VEGEN2W VEGEN2X VEGEN2Y RRC4CPU0 RRC4CPU1 RRCREV	VGEN2 VGEN2 RRC-4	2/1998 2/1998 2/1998 - - - -	B/WVgen ATV B/WVgen ATV B/WVgen ATV  DTMF/CTCSS  DTMF/line P.Patch	video gen., real time clock video gen., real time clock video gen., real time clock  FM RPT repeater controler CPU-0 FM RPT repeater controler CPU-1 FM RPT repeater REVERSE module radio telefone interface		
Index:	band - Fr RTX - Us ME - N L - La display - LC	- Source file name (ime datoteke) - Frq. band (frekvencni pas) - Used for (namen uporabe) - Number of memries (stevilo spominov) - Lock detect *=yes ( izpis LOCK bita) - LCD print out (izpis na LCD) - RealVCO frq. (realna		Q2 JPI step PLL_ic rem/IF	<ul> <li>Used Q2 crystal (MHz)</li> <li>Jumper setting (polozaj mostièka)</li> <li>Channel step (kanalni korak)</li> <li>Used PLL circuit (uporabljeno vezje)</li> <li>(medfrekvenca)</li> <li>New version</li> <li>Brand new sofware</li> </ul>	

New software releases: LANC-I vI.I0 1999-II-25

Tiny remote controller for Digital8 and DV camcorders.

### UNII3PTL vI.30 2000-02-25

Software for UNIPLL based 13cm ATV TX. PLL frequency range 1.0-2.7GHz Philips TSA-5055T used, the same functions as 23cm version + 64 mem. & ADC.

# UNIPLL - Universal PLL board

#### Software & Hardware info 1996 - 2000

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UNIPLL is a universal PLL board cpu controlled, with LCD, keyboard and TSA-5511 PLL (optional with ext. SAD6102 or TSA5055T). It will convert your old ATV transmitter to a finest digitaly controlled ATV transmitter. All you need is one small board, few components and cpu with program. Why UNIPLL for ATV? Becouse you can any time see exact frequency and pll\_lock state., You will have enough memory for independnt storage of yor favorite ATV frequencies. UNIPLL offers user friendly control of your ATV TX via keyboard and will add new qualities to your ATV system. It is also the best ATV pll project in this area.

UNIPLL board is based on: MICROCHIP PIC-16F84 cpu, Philips made and wide available PLL ic TSA-5511 in 18pin DIL, two or one line Hitachi compatible LCD, 24C02 (24C04...) or similar EEPROM, 4 keys (pushbuttons) for tuning frequency, selecting memories & menu options, Murata Piezzo, 2 xtals and few other components (res., cap, etc). CPUs are preprogrammed with one selected software from previus UNIPLL software table. All programs are large and totaly fills CPU program area. Single UNIPLL CPU can hold one single software at time.

#### Common functions of UNIPLL programs are:

- · All sofware versions below VCO frq. 1.6 GHz uses the same UNIPLL board, size 5.8 x 9.2 cm. PLL is a TSA-5511, on board. Crystal Q2 must be properly selected from software table, also the position of JP-1.
- All software versions above VCO frq. I.6 GHz uses only CPU part of UNIPLL board. PLL is a SDA-6102 or TSA-5055T (SMD) sodered on external HF board (not supplied). Both IC's uses external parts for Ud tuning, near circuit. Power supply and I2C bus of PLL are connected to the main UNIPLL board.
- · A 10GHz version drives VCO on 2.5 GHz frq, this signal source must be multiplied by 4 on to 10 GHz.
- · A 5GHz version drives VCO on 1.9 GHz frq, this signal source must be multiplied by 3 on to 5.7 GHz.
- · A 13cm ATV TX with TSA-5055T PLL uses 4MHz reference crystal on PLL IC.
- · All versions uses the same 4 keys (push buttons) and there are the same main functions on those keys.
- All keys have higher AUTO\_REPEAT speed in VFO mode, and slower in MEMORY mode.
- · Any pressed key will produce short beep, error on I2C bus and any other error will produce additional beeps.
- There is the same way of displaying parameters on LCD, as follow: FFFF.FFF CC M L example: 2330.000 01 M 1280.500 31 V L 434.400 00 M L ..... 105.400 09 M L F=frequency, CC=memory number, M=memory mode / V=vfo mode, L=lock / ?=unlock
- From version 1.20 and up are bulid in software additional LCD messages like:

At power\_on: Software name & authors callsign At Info cmd: Version & date of actual software

At total init: **EEPROM Init -> ....** 

- Each software offers to user at least 32 memory places, one VFO with both frequency limits, Function\_key for switching between MEM /VFO modes, plus separate MR / MW functions.
- After power on, or reset, MEM (memory) mode is selected, and switched to memory location "00".

UNIPLL & ATVRX2 keyboard commands:

Memory mode VFO mode Key+Pwr\_ON DOWN<br/>memory\_downUP<br/>memory\_upF<br/>VFO modeMR/MWfreq.\_downmemory\_upVFO modeMemory READfreq.\_downfreq.\_upMEM modeMemory WRITEsoftware versiontoggle LCD typeEEPROM InitIF N/W (ATVRX2)

· UNIPLL software versions uses additional Power\_ON commands

Build in all versions: **F+Power\_ON** initializes EEPROM circuit (first run?).

Version I.20 and up: **DOWN+Power\_ON** displays software version

· Changing LCD type (Hine/2lines) from version 1.30 and up only

For software that uses SDA-6102 PLL ..... on keyboard with UP+Power\_ON cmd

TSA-5055T PLL, and ATVRX-2 v1.30

For software that uses TSA-5511 PLL ..... on PLL: with connecting 220E resistor from PIN-9 of

PLL to the GND or +5v

Ov on PIN-9 = 1 line LCD.
+3-5v on PIN-9 (or left open) = 2 line LCD or 2

ctrl. chip on LCD.

- · All software versions (except those using SDA PLL circuit) have build in detection (and print out on LCD) of VCO's PLL\_LOCK state. Versions with the last character **L** in the name, displays this state in real time (few times in second). Version without last character **L** in the name displays this state in the start of UNIPLL board, or at pressing of any UNIPLL key.
- · If there is not usable PLL or EEPROM circuit, UNIPLL will announce this Error with few beeps.

Programs for ATV will have main step 62.5KHz (23cm) and 80KHz (13cm, 6cm, 3cm), but for this needs active step on keyboard is switched on to 0.5MHz on 23cm, 0.640MHz on 13cm, 1.2MHz on 6cm, and on to 1.6MHz on 3cm band.. On ATVRX2 is channel step set up on to 1MHz, and on UNI13PTL version is step 0.5MHz.

Program for 70cm high speed (38k4) user packet radio WBFM rtx (UNI70W) supports 1st IF at 36.000 MHz and VFO steps 50KHz. Current žfone' versions of UNIPLL software (25k step) don't support common RTX, except by using HF mixing methods with sigle frequency VCO for RX as well for TX mode.

UNIPLL module is not usable on PSK equipment due to poor PLL loop noisie and stability conditions.

PCBs are professionaly made and are available from author, also the preprogramed CPUs. Source files for UNIPLL are normaly not available, except for mass reproduction.

The UNIPLL project was published in Slovenian HAM radio magazine: CQ-ZRS 3/96 in June 1996. Schematics files for printing on HPLaserJet or matrix printer can be found in ATVRC directory on S50ATV file server BBS. To access on S50ATV BBS use command: C S50ATV, on any S5 high speed node. This and many other S51KQ projects can be find on Internet address: <a href="http://lea.hamradio.si/~s51kq">http://lea.hamradio.si/~s51kq</a>







# PCGENX - Universal I2C controller

#### Software & Hardware info 1996 - 1999

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PCGENX is an universal cpu controller board with I2C bus. It was designed for controlling of CGEN video generators, but latter was used in some other applications too. On board are main cpu, DTMF decoder, Beep generator, I relay, 2 jumpers and connector for RTX (audio IN, OUT, /PTT), and one for 4 button keyboard.

At the moment exists following software for PCGENX board:

<u>File name</u>	project	<u>CQ-ZRS</u>	<u>mode</u>	<u>usage</u>	<u>remarks</u>	<u>Software</u>
PCGENX PCLOCK2	PC GENX	2/1996	DTMF/KEYB	ATV	cgen 2,3,4,5,6 controler, I2C bus	
PCVOICE	PCGENX	-		ГМ 4		v1.01
		-	Voice BEACON	FM rpt	3 voice msg.,AUTO + DTMF remote	
PCCAM	PCGENX	-	CAMERA contrl	ATV	DTMF remote, $2x$ ON/OFF + L,R	vI.00

**PCVOICE** is a new software. It enables usage of PCGENX and additional 3 ISD circuits, as an automatic VOICE becon (identifier) on fone repeater site, or as stand alone voice beacon. Software enables varius USER commands as well as SYSOP remote access with pasword and full controll over the system. Available DTMF commands are:

User commands	- AI A2 A3 A0	Play: MSG1(ID), MSG2, MSG3, Check setup		
Sysop commands	B0 B1 B2 B3 B8	Enable BEACON, Cmd-A1, Cmd-A2, Cmd-A3, Cmd-A0		
	C0 C1 C2 C2 C8	Disable BEACON, Cmd-A1, Cmd-A2, Cmd-A3, Cmd-A0		
	xxxxx	6 digit fixed password: Enter in a SYSOP MODE		
	#	Quit sysop mode. Auto QUIT occur after 20s of no activity!		

#### Common functions of PCVOICE program are:

- · After Power\_ON system will go ON with short molody following by playing MSGI(ID) from first ISD bank. This even occur regardless to the system setup.
- SYSOP can access to the system setup remotly with 6 digit DTMF password. In SYSOP MODE each executed command will be confirmed with "R" for 1, or "O" for value 0 in CW.At the end of any DTMF entry system will announce SYSOP MODE with "S" in CW. Sysop can manualy terminate sysop mode with DTMF command "#". Or it will be automaticly terminater after 20s of no activity.
- Sysop can remotly ENABLE or DISABLE all user commands, plus Automatic beacon identification and system check command (A0). All predefined functions are saved to the internal EEPROM. After power failure PCGENX will start\_up to the same state as was before failure.
- AUTOMATIC BEACON INDETIFICATION will play if enabled, after (each) 70 minutes of no DTMF activity, Voice mesage from bank-I (MSGI), following by "K" in CW. Any DTMF code on input will reset beacons timer to 0 (cont begins from 0).

 By executing SYSTEM CHECK command (A0), repeater or system will go ON\_AIR with short melody, following with Telemtry data in CW, as follows:

#### - short melody

- On board LED will shortly blink if the BEACON is ON. Any presence of DTMF on input will put LED ON as long DTMF tones exists. If the PCGENX goes ON\_AIR LED will go ON too.
- · HOW TO CONNECT ISD1416P to the PCGENX board?

ISD1416P: Pins 1-6, 9-10, 12-13 and 26 are connected to GND

Pins 16, 23 and 27-28 are connected to the +5v

Pin 14 is the Audio OUT, via 10nF from each ISD to common point, and out to the TX

Pin 24 is connected to the PCGENX board as follow:

ISD-1 pin 24 (first bank) to the SCL connector point ISD-2 pin 24 (second bank) to the SDA connector point ISD-3 pin 24 (first bank) to the hot pin of jumper JP1.

Jumper JP2 stays OPEN. Don't connect any keyboard on PCGENX if the PCVOICE software is installed! For how to make simple interface, and how to record Voice messages in to IDS circuits, take a look in ISD1416P PDF data file available on WWW.

**PCCAM** is a new software too. It enables usage of PCGENX board and additional 3 RELAYS, as an simple remote control unit for video camera and ATV tx. Software enables few USER DTMF commands and one timer, as follows:

User commands	11 10	On board RELAY-0	ON OFF	
	21 20	Ext. RELAY-I	ON OFF	
	3	Ext. RELAY-2	ON / OFF	// Real time execution
	4	Ext. RELAY-3	ON / OFF	// Real time execution
	51 50	Auto TIMER on RELA	-0 ON OFF	

#### Common functions of PCCAM program are:

- · After Power\_ON external RELAY-I will go on state as was at time power failure occur. If the TIMER is enabled, LED will shortly blink and the RELAY-0 will be put OFF after caa 10 minutes of no activity. Any DTMF tone on input will put LED ON. Command "3" and "4" are for moving camera left and right. There is no answer\_back via AIR build in, as done in PCGENX or PCVOICE versions.
- HOW TO USE PCCAM software with PCGENX board?

  At first place two 100E resistors at I2C conn. must be changed with 5K6. Resistor 39K near I2C conn. shuld be removed. Resistor IK righth of JPI must be changed to the 5K6. EXTERNAL NPN transistors with relays shuld be connected as on board relay (transistor BC337). RELAY-I (transistor) to the SCL, RELAY-2 to the SDA and RELAY-3 to the hotest pin of JP-I.JP-2 remains OPEN. Don't connect keyboard on PCGENX. Do not forget to put protection diode on each external relay, as done on board!

#### Projects based on 80c535 microcontroller:



 File
 project
 CQ-ZRS
 mode
 usage
 remarks
 Software

 HCALL
 HCALL
 POCSAG Ik2/2k4
 HAM
 Amateur radio Paging server
 v0.90

#### **Projects based on ATMEL AVR:**



 File name
 project
 CQ-ZRS
 used cpu
 usage
 remarks
 Software

 VID2G
 VID2G
 I+2/1999
 ATmega103
 ATV
 PAL / NTSC video identifier & gen.
 v2.01

#### S51KQ sofware for Windows 95/98:

EXE file	project	CQ-ZRS	<u>usage</u>	<u>remarks</u>	<u>Software</u>
CGEN	CGEN-xx	4/95, 1/96	ATV	Manager for CGEN modules	v1.00
VI PS	ATVRC	5/1996	ATV	Win manager for home usage	v1.00
VID2w20I	VID2G	1+2/1999	ATV	PAL / NTSC video identifier & generator manager	v2.01
UpgVID2G	VID2G	1/2000	ATV	VID2G operating system upgrade tool	v1.00
VID01w10	VID-01	3/1994	ATV	Compiler for EPROM of VID-01 b/w inserter	vI.00

CGEN VIPS VID2G UpgVID2g VID01w











NEW CPU source files (BIN, HEX) available with reproduction rights.

### NEW UNIPLL-2 under developement NEW

With large LCD, multiband user selectable software 10m - 3cm will be build in, RTX shift support, VFO steps from 5kHz and up, varius IF support, full RS-232 access, memory download/upload, Windows software, etc..

Visit our WWW site in future!

Internet: http://lea.hamradio.si/~s51kq

More than 43 Mb of S51KQ stuff, photos, shematics, EXE files, programs,...

Full internet home page image, full S50ATV Ax25 BBS image,

On S51KQ CD:

Control of the Contro

large PDF library, picture collection, DSP3MV collection, additional software, AVI files, etc etc.

Full - 640 Mb of tech files on S51KQ CD ...





